8. The display defined in claim 7 wherein the inactive area is a ring-shaped area that surrounds the active area.

9. The display defined in claim 1 wherein the display driver circuitry includes a shift register having an output driver in each row of pixels that supplies a gate line signal to the gate line in that row of pixels.

10. The display defined in claim 9 wherein the array of pixels has a curved edge and is surrounded by a ring-shaped inactive area and wherein the shift register forms part of a gate driver circuit that extends along the curved edge in a curved strip under the ring-shaped inactive area.

11. The display defined in claim 1 wherein the first vertical segment of each of the folded data lines is in a different one of the halves than the second vertical segment of that folded data line.

12. A display, comprising:

a circular array of pixels;

display driver circuitry;

folded vertical data lines each of which supplies data signals from the display driver circuitry to first and second columns of the pixels; and

horizontal lines that supply control signals to rows of the pixels, wherein each row of pixels is supplied with control signals by a respective one of the horizontal lines and wherein the horizontal line that supplies the control signals to that row of pixels has a left half that supplies the control signals to a left half of the pixels in that row and a right half that is electrically isolated from the left half and that supplies the control signals to a right half of the pixels in that row.

13. A display, comprising:

a circular array of pixels;

display driver circuitry;

data lines each of which has a first segment, a second segment that runs parallel to the first segment, and a coupling segment that couples the first and second segments together, wherein the first segment supplies data signals from the display driver circuitry to a first column of the pixels and wherein the second segment supplies data signals from the display driver circuitry to a second column of the pixels; and

horizontal lines that supply control signals to rows of the pixels, wherein each row of pixels is supplied with control signals by a respective one of the horizontal lines and wherein the horizontal line that supplies the control signals to that row of pixels is divided into electrically isolated left and right segments.

14. The display defined in claim 13 wherein the left segment supplies the control signals to a left half of the pixels in that row and wherein the right segment supplies the control signals to a right half of the pixels in that row.

15. The display defined in claim 14, wherein the left segment and the right segment of each horizontal line are collinear.

16. The display defined in claim 12, wherein the left half of each horizontal line is coupled to a first gate driver circuit on the left side of the display and wherein the right half of each horizontal line is coupled to a second gate driver circuit on the right side of the display.

17. The display defined in claim 12, wherein the left half of each horizontal line is collinear with the right half of that horizontal line.

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